

**Amendments to the Specification:**

Please amend the paragraph beginning at page 18, line 14 as follows:

Referring to Figure 10, which shows another alternate embodiment, each one of the switches 18 includes a plurality of controller devices 100. Each of the controller devices 100 on a switch 18 is able to communicate over the switch fabric 109 with each and every other controller card 100 on the other switches 18. Alternatively, each of the controller devices 100 are able to communicate with each and every other controller device 100 over network 141 **[[14]]**.

Please amend the paragraph beginning at page 18, line 23 as follows:

In this embodiment, referring to Figure 12B **[[16A]]**, the controller device 100 includes a Central Processing Unit (CPU) 102, a cache memory module 104) used as a data cache and for supporting processing and a system interconnect interface 103 for connecting the CPU 102, cache memory 104 and communication ports 109. The controller device 100 typically includes at least one communication port 109 used to communicate with the external network, other controller devices or other peripherals. For example, the communication port 109 may be comprised of a SAN port 111 for communication with physical disks 110, a WAN port 107 used to connect with the wide area network (WAN), a server port 108 used to connect with servers, and at least one additional communication port 109 used for communication between a plurality of controller devices 100. The system interface 103 may typically be comprised of a Field Programmable Gate Array (FPGA) or Application Specific Integrated Circuit (ASIC). An optional computation engine 112 may be included on the system interface 103 which is used to accelerate operations such as RAID check sum calculations, encryption, and compression, and assorted data routing and support chips.

Please amend the paragraph beginning at page 21, line 9 as follows:

In another embodiment, yet another form factor, as shown in Figure 14, the Host Based Adapter (HBA) implementation 140, one or more controller devices 100 reside inside the host or server

12 and are interconnected via the host input/output bus 169 ~~[[141]]~~, typically a PCI. The interface to the FEN is via ports 109 coupled to the I/O buss 169 via a controller card 100.

Please amend the paragraph beginning at page 24, line 3 as follows:

At step 300, a request is sent from the client 10 to a controller device 100, or to server 12 communicating with the client 10, over the FEN 14. The messaging protocol 301 is typically an ethernet packet 70, such as an HTTP request. At step 302, the request is received by the controller device 100, or the server 12, and processed. At step 304, a notification message 303 is sent from the server 12, or from controller device 100, to the appropriate controller device 100' on the SAN 141 (e.g., Figure 14) that is responsible for streaming the required data to the client 10. The controller device 100 or server 12 typically includes an HTTP look-up engine for locating which controller device 100' has the required data.